

DEPARTMENT OF COMPUTER SCIENCE
GOVT. HOLKAR AUTONOMOUS SCIENCE COLLEGE INDORE

M.Sc. (C. S.) I Semester
Core-1 Computer & Communication Fundamentals
2018-2020- and onwards

Unit I

Computer Organization : Digital and Analog computers, Major components of a digital computer, Memory addressing capability of a CPU, Word length of a computer, Processing speed of a CPU, Definitions of Hardware, Software and Firmware. Definitions of Dumb, Smart and Intelligent terminals. Binary Systems: Digital Systems, Binary Numbers, Number Base Conversions, Octal and Hexadecimal Numbers, Complements, Signed Binary Numbers, Binary Codes: BCD code, Gray Code, ASCII code, Excess 3 Code, Error detecting Code.

Unit II

Computer Arithmetic : Binary representation of Negative Integers using 2's complement and Signed magnitude representation, Fixed point Arithmetic operations on Positive and Signed (Negative) Integers like addition, subtraction, multiplication.

Boolean Algebra and Logic Gates : Basic Definitions, Basic Theorems and properties of Boolean Algebra, Boolean Functions, Canonical and standard forms, Other Logic operations, Digital Logic gates, Integrated Circuits. Gate-Level Minimization: The K-Map Method, 3 and 4 variable K-Map, Product of sums simplification, Sum of Products simplification, Don't care conditions, NAND and NOR implementations, Exclusive-OR function.

Unit III

Combinational Logic: Combinational Circuits, Analysis Procedure, Design Procedure, Binary half adder, binary full adder, binary full subtractor, binary parallel adder, carry propagation delay and Propagation delay calculation of various digital circuits. Carry look ahead generator fast adder, Decimal Adder, Binary multiplier, Magnitude comparator, Code converters like binary to gray, BCD to excess 3. Decoders, Encoders, Multiplexers, Demultiplexers.

Unit IV

Analysis of clocked sequential circuits: State diagrams, State equations for D, JK and T Flip flops. State reduction methods using all Flip Flops. Mealy and Moore Models. Shift Registers- Serial in Serial out, Serial in Parallel out, Parallel in Serial out and Parallel in Parallel out. Designing of Asynchronous (Ripple) Counters, Design of Synchronous Counters. Synchronous Sequential logic : Sequential circuits, Latches, Flip Flops : SR, D, JK, T, Master Slave JK Flip flop. Characteristic equations and Excitation tables of flip flops.

Unit V

Communication Systems: Basics of communication systems, Types of communication, Transmission impairments, analog vs. digital transmission, requirements of communication systems, channel capacity. Shannon's theorem. Data rate of a channel, Physical Communication Media- Bounded Media: Twisted Pair, Coaxial Cable, Optical Fiber. Unbounded Media – Microwave Communication, Radio wave Communication, Satellite Comm. Time Division Multiplexing and Frequency Division Multiplexing. Data communications and its components, Half Duplex and Full Duplex Transmission. Asynchronous and synchronous transmission LAN, MAN, WAN. Network Topologies- Bus, Star, mesh, Ring. Categories of networks: Introduction of Communication Protocols like OSI and TCP/IP model.

Required Text(s):

- Digital Design by M. Morris Mano. Third addition
- Computer Architecture By Dr. Rajkamal.
- Data communications and networking By A. Forouzan
- Computer Fundamentals – Architecture and Organisation By B. Ram.
- Computer networks by Andrew Tanenbaum
- Principles of digital communication system & computer networks By K.V.K.K. Prasad
- Computer organization and architecture by William Stallings.

[Handwritten signatures and initials in blue ink, including names like 'Rajkamal', 'Forouzan', 'Tanenbaum', 'Prasad', 'Stallings', and various initials.]

DEPARTMENT OF COMPUTER SCIENCE
GOVT. HOLKAR AUTONOMOUS SCIENCE COLLEGE INDORE

M.Sc. (C. S.) I Semester
Core-3 Operating System
2018-2020 and onwards

Unit I

Introduction: Evolution of operating systems, operating system concepts, operating system Services, System Calls. Batch processing, time sharing operating systems, real time systems.

Process Management: Process Concept, Scheduling, operations on process, cooperating process, IPC.

CPU Scheduling: basic Concepts, Scheduling Criteria & Algorithms.

Unit II

Concurrent Process: Mutual Exclusion, Synchronization. Techniques of inter process communication, message driven operating systems. Deadlock handling techniques.

Unit III

Memory Management: Concepts, Single user memory management. Partition memory allocation. Virtual memory management using paging and segmentation techniques, Virtual Memory Concepts.

Unit IV

File Management: Operations on a file, Structure of File System, File Access Methods, Directory structure , sharing and protection of file , Directory structure & implementation, Allocation Methods, Free Space Management.

Unit V

Device Management: Goal of input /output software design, Structure of device hardware and software. Layers of I/O software. Structure of device driver, disk driver, disk arm scheduling Algorithms, terminal driver, function of clock driver, printer, mouse, scanner etc.

Case Studies: Unix/Linux, Windows operating system.

Required Text(s):

- A. Silberschatz and P. Galvin ,Operating System Concepts, 6th Edition, Addison Wesley, 2003.
- William Stallings, Operating systems, 4th Edition, Prentice Hall, 2000.
- D.Dhamdhare, Operating System: a concept based approach, 1st Edition, Tata McGraw Hill, 2003.
- A.S. Tanenbaum , Modern Operating System, 3rd Edition, Prentice Hall of India

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DEPARTMENT OF COMPUTER SCIENCE
GOVT. HOLKAR AUTONOMOUS SCIENCE COLLEGE INDORE

M.Sc. (C. S.) I Semester
Core-4 Programming & Problem solving Through "C"
2018-2020 and onwards

Unit I

Overview of Problem solving: Introduction to computer based Problem solving, Programming concepts with flowcharting and algorithms , classification of Programming languages, Programming environment {Assemblers, compilers, interpreters, linkers and loaders}. Developing and debugging flowcharts for programming problem.

Unit II

Fundamentals of C programming: Overview of C - Various constructs of C program, coding style, data types, constants and variables, expressions and operators, basic input/output operations and formatting characters, decision making and branching, looping constructs, Arguments to main, Enumerations and bits fields, typedef, type casting, Storage class.

Unit III

Array and their Applications: Arrays {one dimensional and multidimensional array}, String Handling, Searching and sorting techniques, matrices operations.

Unit IV

Advanced Programming Concepts: Structures and union, Functions {Standard and User defined function, parameter passing, scope rules}, Recursion {Using recursion, conversion of recursive program to non-recursive}. Dynamic memory allocation and pointer {Uses, pitfalls, pointer to various user defined and standard data types}.

Unit V

More Advanced Programming Concepts: Pre-processors {define, include, macro's, ifdef...}. Introduction to file handling. Header files creation, Graphics.

Required Text(s):

- B.W. Kerighan & D.M. Ritchie, The C programming Language, 2nd Edition Prentice Hall, 1998.
- Herbert Schildt, C++ The Complete Reference , 4th Edition McGraw-Hill 2000.
- Yashavant Kanetkar, Let Us C, 8th Edition, Infinity Science Press 2008.
- Ashok N. Kamthane, "Programming with ANSI and Turbo C", Pearson Education.

Handwritten notes and signatures in blue ink:
- Top left: "25/06/18" and "Ratna" with a signature.
- Middle left: "A", "B", "C", "D", "E", "F", "G", "H", "I", "J", "K", "L", "M", "N", "O", "P", "Q", "R", "S", "T", "U", "V", "W", "X", "Y", "Z" with various marks.
- Middle right: "Curry" with a signature.
- Bottom right: "sh" and "Anurag" with a signature.